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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/750,385	12/31/2003	Lance Weston	H0006069-555	8983	
HONEYWELL INTERNATIONAL, INC. LAW DEPARTMENT			EXAMINER		
			DINH, TUAN T		
101 COLUMBIA ROAD MORRISTOWN, NJ 07692			ART UNIT	PAPER NUMBER	
				2841	
			MAIL DATE	DELIVERY MODE	
			04/29/2009	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)		
	10/750,385	WESTON ET AL.		
Office Action Summary	Examiner	Art Unit		
	Tuan T. Dinh	2841		
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	correspondence address		
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailir earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION 136(a). In no event, however, may a reply be tir will apply and will expire SIX (6) MONTHS from the, cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).		
Status				
1) ☐ Responsive to communication(s) filed on 15 J 2a) ☐ This action is FINAL . 2b) ☐ This 3) ☐ Since this application is in condition for allowated closed in accordance with the practice under	s action is non-final. ance except for formal matters, pro			
Disposition of Claims				
4) Claim(s) 1-12 and 27-89 is/are pending in the 4a) Of the above claim(s) 1-12 and 27-61 is/are 5) Claim(s) 71-75 and 85-89 is/are allowed. 6) Claim(s) 62-70,76-84 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or are subjected to by the Examination The drawing(s) filed on is/are: a) are acceptable.	re withdrawn from consideration. or election requirement. er.	Examiner.		
Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E	ction is required if the drawing(s) is ob	jected to. See 37 CFR 1.121(d).		
Priority under 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 				
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D: 5) Notice of Informal F 6) Other:	ate		

DETAILED ACTION

The Appeal Brief filled on 01/15/09 is acknowledged, Appellant's arguments are persuasive, and the prosecution is now reopened.

Drawings

1. Figure 2 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

- The following is a quotation of the second paragraph of 35 U.S.C. 112:
 The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 2. Claims 63, 67, 72, 77, 81, and 86 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claim 63, lies 2-4 is unclear. What does applicant mean of "second edge of the trace and first edge of the second cap are both substantially equal to X"?

Does applicant mean either the dimension or size of the second edge of the trace and the first edge of the second cap are both substantially equal to X (0.1 in).

Rejected claims 67, 72, 77, and 88 are similar to claim 63.

Please clarify.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 62-70, 76-84 are rejected under 35 U.S.C. 103(a) as being unpatentable over Laschinski (U.S. Patent 6,467,163) in view of Tanabe et al. (U.S. Patent 4,883,920).

As to claims 62, 76, Laschinski discloses a universal component mounting structure for surface mountable electronic devices as shown in figures 1-2 comprising:

a printed circuit board (2, column 3, lines 60-61) having a SMT component (16) mounted on, the component (30; 40) having first and second terminals (column 5, lines 23-24), and each terminals having a first edge;

a conductive trace (10) formed on the PCB (2) having first and second opposed edges extending intermediate said first and second terminals (see figure 2), the edges of the trace (10) being defined a plane, see figure 2 and intersecting the first edge of the first and second terminals, the edge of the trace (10) disposed in parallel spaced relative to the edge of the first and second terminals respectively.

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Laschinski does not specifically disclose the SMT component (16) having end caps. Tanabe et al. teaches a SMT component (2) having end caps (4) mounted on a substrate (1). It would have been obvious to one having an ordinary skill in the art at the time the invention was made to have the SMT component having end caps as taught by Tanabe et al. employed the SMT component of Laschinski in order to easy install components mounted on the PCB.

As to claims 66, 80, Laschinski discloses a universal component mounting structure for surface mountable electronic devices as shown in figures 1-2 comprising:

a printed circuit board (2, column 3, lines 60-61) having a first SMT component (16) mounted on, the component (30; 40) having first and second terminals (column 5, lines 23-24), and each terminals having a first edge; and a second SMT component (14) having end caps (see figure 2), and

a conductive trace (10) formed on the PCB (2) having first and second opposed edges extending intermediate said first and second terminals (see figure 2), the edges of the trace (10) being defined a plane, see figure 2 and intersecting the first edge of the first and second terminals, the edge of the trace (10) disposed in parallel spaced relative to the edge of the first and second terminals respectively.

Laschinski does not specifically disclose the SMT component (16) having end caps. Tanabe et al. teaches a SMT component (2) having end caps (4) mounted on a substrate (1). It would have been obvious to one having an ordinary skill in the art at the time the invention was made to have the SMT component having end caps as

taught by Tanabe et al. employed the SMT component of Laschinski in order to easy install components mounted on the PCB.

Regarding claims 63-65, 67-70, 77-79, and 81-84, Laschinski as modified by Tanabe et al. do not discloses either the second edge of the trace and the first edge of the second cap substantially equal to X (X = 0.01 in).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have X dimension of 0.01 in equal to either the second edge of the trace and the first edge of the second cap in order to minimize size of the PCB, since such a modification would have involved a mere change in the size of a component. A change in size is generally recognized as being within the level Of ordinary skill in the art. In re Rose 105 USPQ 237 (CCPA 1955).

Allowable Subject Matter

5. Claims 71-75, and 85-89 are allowed.

The following is an examiner's statement of reasons for allowance: the prior arts do not teach or suggest in combination of the system having a second conductive trace on a printed circuit board having first and second opposed edges extending intermediate first and second end caps of a third surface mount component, said first and second opposed edges of said second conductive trace being coplanar and thereby defining a plane, said plane intersecting said first edge of said first end cap of said third surface mount component and intersecting said first edge of said second end cap of

said third surface mount component, said first edge of said second trace being disposed in parallel spaced relation to said first edge of said first end cap of said third surface mount component and said second edge of said second trace being disposed in parallel spaced relation to said first edge of said second end cap of said third surface mount component (claims 71 and 85).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Response to Arguments

6. Applicant's arguments with respect to claims 62-70, 76-84 have been considered but are most in view of the new ground(s) of rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Dinh whose telephone number is 571-272-1929. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Reichard Dean can be reached on 571-272-1984. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Tuan T Dinh/ Primary Examiner, Art Unit 2841.